



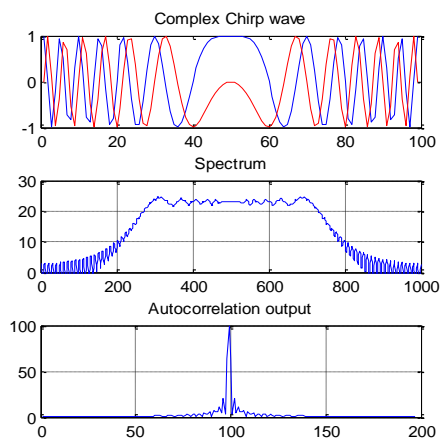
Overlap and Save Pulse Compression Design Example

1.0

Design Example Datasheet

Description

Pulse compression is a signal processing technique to increase the signal and noise ratio, detection range and range resolution. It is done so by sending a long wideband signal at transmit, then correlates the receive signal with the transmit signal. When the two signals match in time, a sharp peak is produced.



The cross correlation can be performed by time-domain convolution, or FFT-based fast convolution which has lower complexity of order $\log(N)$ instead of N , where N is the length of the waveform. Following table shows the multipliers count of both methods.

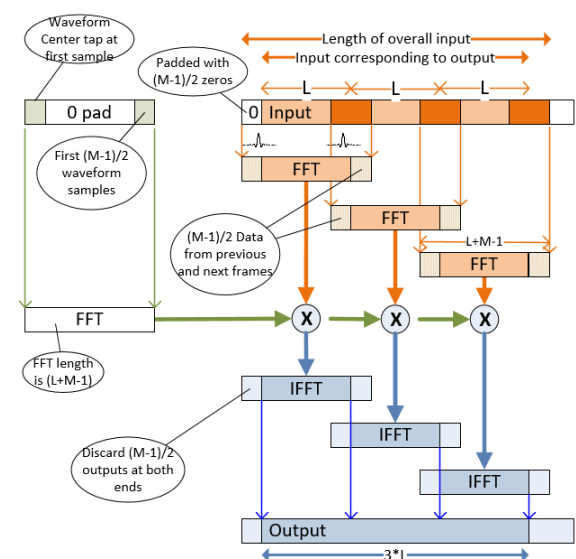
Linear Conv, N	$\log_2 N$	Real mult. per FFT	Total OLS real mult.
16	4	4	20
64	6	8	36
256	8	12	52
1024	10	16	68
4096	12	20	84

However, FFT is block based processing but the radar receive is a streaming one, a technique known as

Overlap-and-Save (OLS), also known as Overlap-and-Discard is used here to partition the input signal and regroup the blocked output to resemble streaming output. OLS is preferred over Overlap-and-Add as adders is not required.

A text book implementation of OLS would retain the front portion of the convolution results. An enhanced version is implemented that retains the more useful middle section of convolution result.

Overlap and Save with $(M-1)/2$ padding zeros



Features

- Custom overlap and save technique to retain the more useful middle portion of the convolution results
- Dynamically configurable FFT size, and waveform size.

Applications

- Generic streaming convolutions
- Pulsed radar, automobile radar



101 Innovation
Drive San Jose,
CA 95134

© Intel Corporation. All rights reserved. Intel, the Intel logo, the Intel Inside mark and logo, Altera, Arria, Cyclone, Enpirion, Experience What's Inside, Intel Atom, Intel Core, Intel Xeon, MAX, Nios, Quartus, and Stratix words and logos are trademarks of Intel Corporation or its subsidiaries in the U.S. and/or other countries. Intel reserves the right to make changes to any products and services at any time without notice. Intel assumes no responsibility or liability arising out of the application or use of any information, product, or service described herein except as expressly agreed to in writing by Intel. Intel customers are advised to obtain the latest version of device specifications before relying on any published information and before placing orders for products or services. * Other marks and brands may be claimed as the property of others.

March 2017 Intel